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PRE-APPEAL BRIEF REQUEST FOR REVIEW		Docket Number (Optional)		
		M-15530-2D-2C US		
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail	Application Number F		Filed	
in an envelope addressed to "Mail Stop AF Commissioner for Patents, P.O. Box 1450, Alexandria, VA 21313-1450" [37 CFR 1.8(a)]	10/722,694		11/24/2003	
on January 27, 2006 //	First Named Inventor			
Signature	Sanjai Kohli			
	Art Unit		Examiner	
Typed or printed name Edward C. Kwok	2634		Wang, Ted M.	
Applicant requests review of the final rejection in the above-identified application. No amendments are being filed with this request.				
This request is being filed with a notice of appeal.				
The review is requested for the reason(s) stated on the attached sheet(s). Note: No more than five (5) pages may be provided.				
om the				
applicant/inventor.	-		Signature	
assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed.	Edward C. Kwok			
(Form PTO/SB/96)	Typed or printed name			
attorney or agent of record. 33.938 Registration number			(408) 392–9250	
	Telephone number			
attorney or agent acting under 37 CFR 1.34.	J	anuary 27,	2006	
Registration number if acting under 37 CFR 1.34	-		Date	
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.				
*Total of forms are submitted.				

This collection of information is required by 35 U.S.C. 132. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.6. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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N THE UNITED STATES PATENT AND TRADEMARK OFFICE

S. Kohli; Chen, S.; Cahn, C.; Chansarkar, M.; Turetsky, G.

Assignee:

SiRF Technology, Inc.

Title:

Pseudo-Noise Correlator for GPS Spread-Spectrum Receiver

Serial No.:

10/722,694

Filing Date:

November 24, 2003

Examiner:

Wang, Ted M.

Group Art Unit:

2634

Docket No.:

M-15530-2D-2C US

San Jose, California January 27, 2006

Mail Stop Petition Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REASONS FOR REQUESTING PRE-APPEAL BRIEF REVIEW

Dear Sir:

These reasons support the Pre-Appeal Brief Request for Review filed in response to the Advisory Action mailed on December 28, 2005, maintaining the Examiner's rejection of Claims 21-32.

Claims 21-32 are pending.

In the Final Office Action of July 14, 2005, the Examiner repeated his rejection of Claim 21 under 35 U.S.C. § 102(b) as unpatentable over U.S. Patent 4,426,712 ("Gorski-Popiel"), stating:

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As shown in figures 2-4b, Gorski-Popiel discloses a system and a method for operating a GPS C/A code receiver comprising:

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A plurality of channel means (column 2, lines 50-62), each comprising:

Means (282) for forming x multibit digital segment values per C/A code period, each multibit digital segment value representing a sequential code segments of a received composite of satellite signals (column 4, line 20-column 5, line 39); and

A plurality of correlating means for correlating each multibit digital segment value with n (=10 here) satellite specific set of m differently time delayed segments of C/A code modulation to form at least n time m time delay specific correction values (column 5, lines 35-60), where m is greater than the number of bits (8) in each multibit digital segment value.

The Examiner is mistaken because Claim 21 recites:

21. A system for operating a GPS C/A code receiver comprising:

a plurality of channel means, each comprising:

means for forming x multibit digital segment values per C/A code period, x being an integer, each multibit digital segment value representing a sequential code segment of a received composite of satellite signals; and

a plurality of correlating means for correlating each multibit digital segment value with n satellite specific set of m different time delayed segments of C/A code, n and m being integers, to form at least n times m delay specific correlation values, wherein m is greater than the number of bits in each multibit digital segment value.

(emphasis added)

Applicants explained that Gorski-Popeil's system does not meet the above-underlined limitation, because Gorski-Popeil provides, for each satellite, 8 code position correlations (i.e., m = 8) for each 8 bits of quantized data (see, e.g., Gorski-Popiel, at col. 5, lines 35-52).

That is, Gorski-Popeil teaches 1-bit digital segment values, whereas Claim 21 specifically requires "multibit digital segment values." Thus, the disagreement between Applicants' and Serial No. 10/722,694

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the Examiner's positions is whether or not the limitation "multibit digital segment value" can be met by a "1-bit digital segment value."

In the Advisory Action of December 28, 2005, the Examiner argues that:

- In response to applicant's argument as described in the above paragraph, the examiner recited the limitation of claim 21, lines 4-5, "means for forming x multibit digital segment values per C/A code period, x being an integer." By definition, an "integer" number is a whole number, any of natural number, including all positive numbers, negative numbers, and zero. With x=1, surely it meets the claim 21 limitation as recited in above paragraph. Thus, for the explanation addressed in the above paragraph, the rejection with Gorski-Popiel's references is adequate.

Applicants respectfully submit that the Examiner's argument is irrelevant. The Examiner has confused the variables x and m recited in Claim 21. The Examiner merely showed that the variable x can take on any integer value, including x = 1. However, in Claim 21, x refers to the number of "multibit digital segment values", not to the number of bits m in each "multibit digital segment value." The Examiner's argument has no relevance to the number of bits m in a "multibit digital segment value." The limitation "multibit digital segment value" cannot be met by a 1-bit digital segment value, simply because the word "multibit" – a shorthand for "multiple-bit" — means two or more bits. Thus, the Examiner's rejection is incorrect and must be reversed.

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If the Examiner has any question regarding the above, the Examiner is respectfully requested to telephone the undersigned Attorney for Applicant at (408)-392-9250.

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Attorney for Applicant(s)

Date of Signature

Respectfully submitted,

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